



DOCKET NO. S. KAPUR 5-10

PATENT

#18/C
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Hall

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Sharad Kapur, *et al.*

Serial No.: 09/427,238

Filed: October 26, 1999

For: SYSTEM AND METHOD FOR DETERMINING CAPACITANCE
FOR LARGE-SCALE INTEGRATED CIRCUITS

Group No.: 2123

Examiner: Eduardo Garcia-Otero

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MAY 06 2003

Technology Center 2100

Commissioner for Patents
Washington, D. C. 20231

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(Signature of the person signing the certificate)	

Sir:

PRELIMINARY AMENDMENT

Before re-examination of the above-identified application, please amend it as set forth below.